

10/046073
01/16/02

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10046073	01/16/2002	029	854	3729	Chang Trinh

**APPLICANTS: Murakami Tomoo;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/120,204 07/22/1998 which is us 6,449,838,
issued 9/17/02

** FOREIGN APPLICATIONS VERIFIED: Yes

JAPAN 191737/1995 07/27/1995

PG-PUB ☐ DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed

☒ yes ☐ no

35 USC 119 conditions met

☒ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

Q68028

TITLE : Method of mounting a semiconductor device to a substrate and a mounted structure

U.S. DEPT. OF COMM./PAT & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED

ISSUE FEE	
Amount Due	Date Paid

☐ TERMINAL
DISCLAIMER

Assistant Examiner

Primary Examiner

PREPARED FOR ISSUE

CLAIMS ALLOWED

Total Claims	Print Claim for O.G.
--------------	-------------------------

DRAWING

Sheets Drwg.	Figs. Drwg.	Print Fig.
--------------	-------------	------------

Application Examiner

WARNING: The information disclosed herein may be restricted.
Unauthorized disclosure may be prohibited by the United States Code Title 35,
Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark
Office is restricted to authorized employees and contractors only.

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM
(Attached in pocket on right inside flap)

Best Available Copy